



Job Title: CMOS Analog Engineer / Designer – Imager Systems

Description:

This position is located in Panavision Imaging, LLC, New York design center and will focus on the development of CMOS image sensor products. Panavision Imaging sensor products are targeted at consumer applications such as bar code, machine vision and high-performance digital-still and video cameras. Product development will focus on expanding our current product offerings.

Development tasks include: Imager Architecture, design and simulation of linear analog and mixed-signal circuits-amplifiers, analog references, band gaps, pixel elements, and product transfer to production. Duties include system integration of analog, mixed signal digital processing blocks with imager. Ability to work with vendors, design engineers, and meet project schedules.

Required Skills:

Analog sampled-data circuit design,
Amplifiers, references, band gaps.
Analog/ Mixed signal simulations,
CMOS VLSI,
Product transfer to production,
Tanner, Mentor, Cadence, Synopsis,

Desired Skills:

Imager architecture,
Pixel design,

Minimum Requirements:

MSEE with 5 years minimum in CMOS Mixed Signal Circuit Design, Or PhD with imager related thesis and two years experience in the imager design. Strong knowledge of transistor-level CMOS analog circuit design and simulation (Spice); analog signal processing; signals and systems; and sampling theory.