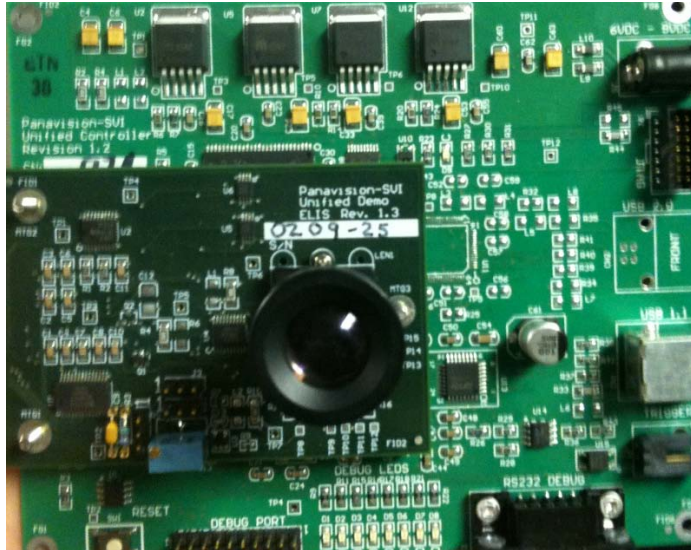




# DLIS-2K XTREMEPIX<sup>™</sup>

## Ultra-Configurable Monochrome Linear Sensor

Unified Demonstration Kit



Features & Benefits

- Ability to operate all Panavision Line Scanners for easy evaluation and measurement
- Communication to Host PC with USB 1.1 control port
- Single +6VDC power Supply
- Single Xilinx FPGA provides all image acquisition, communication, and processing
- Custom IP cores for high speed/precise sampling of Digital/Analog pixel data
- “Super Loop” style firmware used to demonstrate access to IP Cores
- Expandable with knowledge of FPGA design and Xilinx tool-chain
- User I/O Expansion port
- JTAG Debug Port
- RS232 Debug Port
- External Trigger input

DLIS2K Camera Ref-Design Module



Features & Benefits

- High Speed Image acquisition using Panavision DLIS-2K Linear Sensor
- Acquisition rates upto 100MHz / 125MHz
- Acquisition rates above 35,000 FPS
- 10/100 Ethernet Port with DHCP/StaticIP
- Combination of Atmel AVR32 and Xilinx Spartan3AN FPGA (180MHz CPU)
- 32MB of SDRAM
- 8 MB of Flash with User FileSystem
- Linux 2.6.18 kernel
- Onboard Bar-Core Recognition Capability
- Ability to stream image data over UDP Ethernet to remote host
- TCP/IP Command and Control settings for imager setup
- Interoperates with “Setup Scripts” from the Unified Demo Board
- Optional USB 2.0 Peripheral Port
- Optional CANBus 2.0 I/O Port
- User I/O Expansion, LVTTTL Serial, Trigger, and PWM Port
- JTAG Access for Xilinx FPGA/Atmel CPU
- Complete development toolchain, app notes, and user application examples available
- Developer Kit available to help support test and custom application authoring